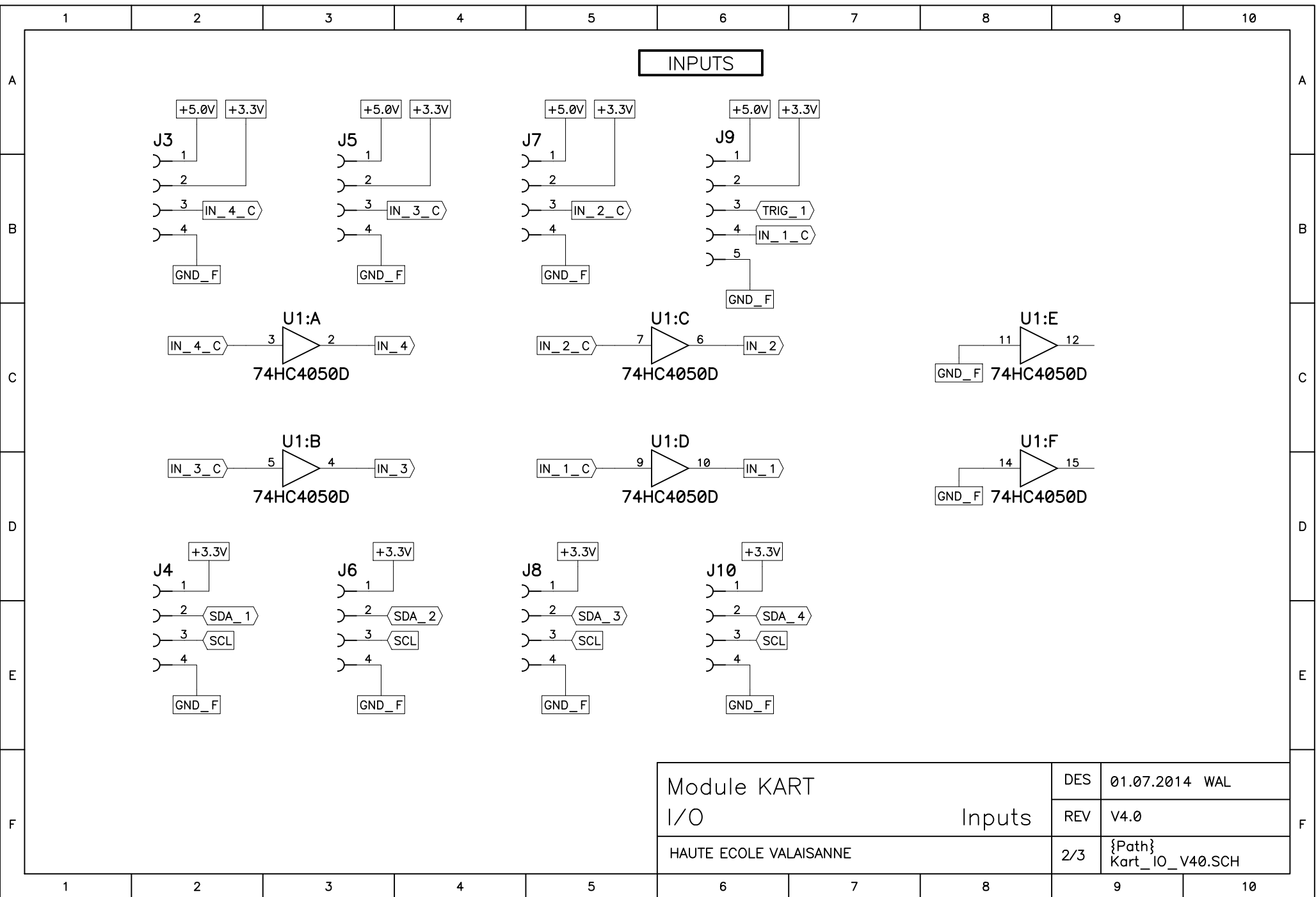


Decoupling capacitor
for U1 (74HC4050D)

Module KART I/O	DES	01.07.2014 WAL
	REV	V4.0
HAUTE ECOLE VALAISANNE	1/3	{Path} Kart_IO_V40.SCH



INPUTS

Module KART

I/O

Inputs

HAUTE ECOLE VALAISANNE

DES 01.07.2014 WAL

REV V4.0

2/3 {Path} Kart_IO_V40.SCH

1 2 3 4 5 6 7 8 9 10

A

A

B

B

C

C

D

D

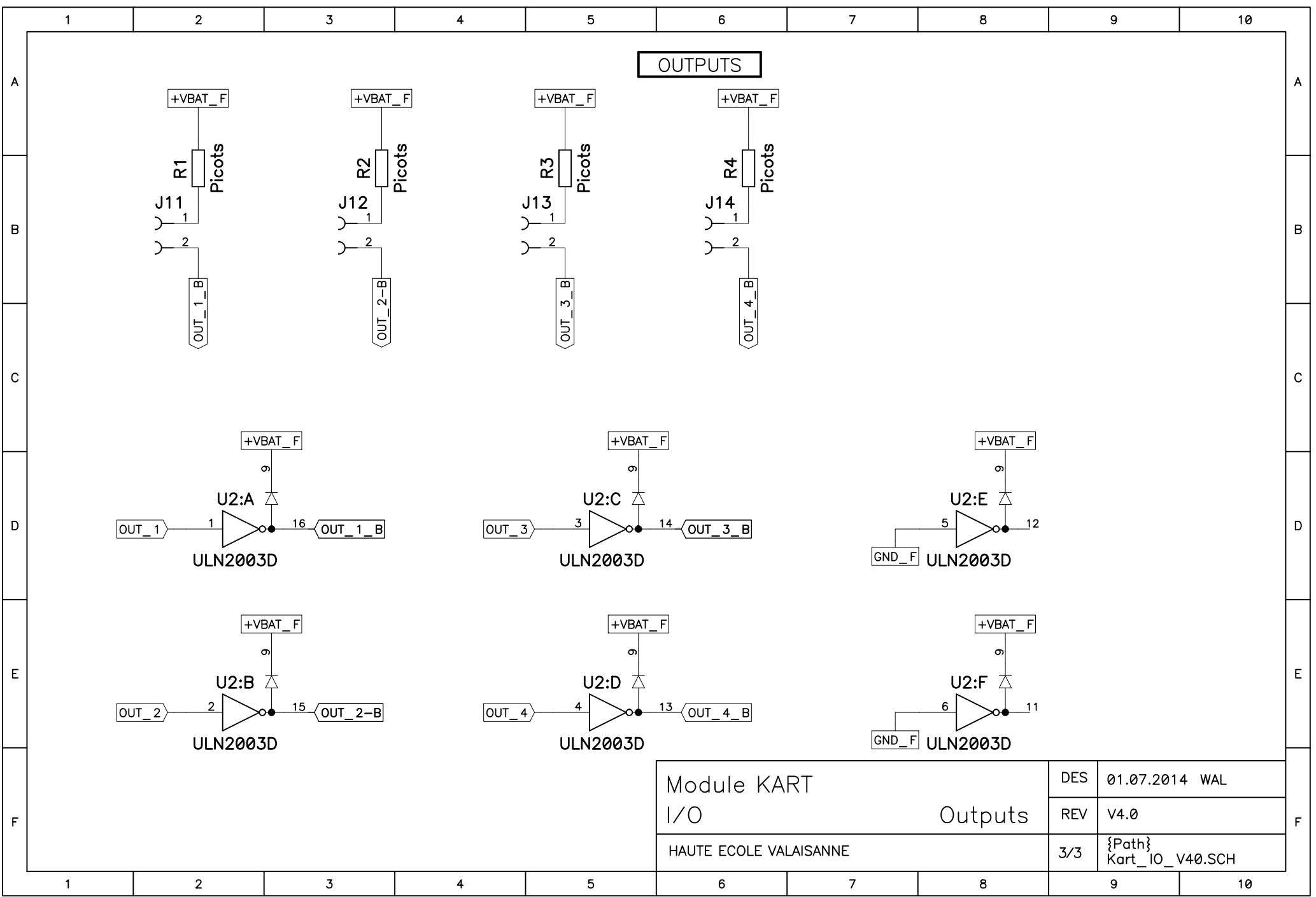
E

E

F

F

1 2 3 4 5 6 7 8 9 10



OUTPUTS

Module KART

I/O

HAUTE ECOLE VALAISANNE

Outputs

DES	01.07.2014 WAL
REV	V4.0
3/3	{Path} Kart_IO_V40.SCH