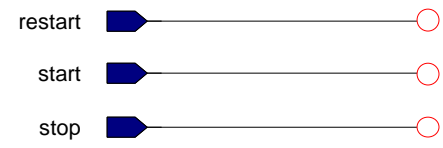


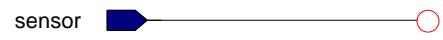
Package List

LIBRARY ieee;
 USE ieee.std_logic_1164.all;
 USE ieee.numeric_std.all;
 LIBRARY gates;
 USE gates.gates.all;

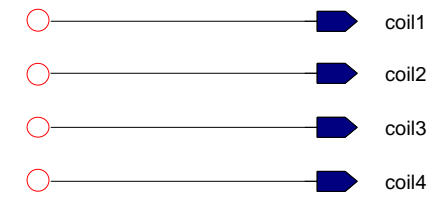
Buttons



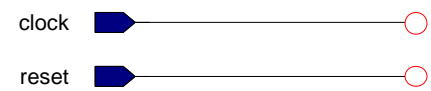
12 o'clock Sensor



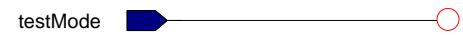
Stepper Motor Coils



Clock & Reset



Testmode only for simulation



Debug Signal (Leds)



Declarations

Ports:

```

clock      : std_ulogic
reset      : std_ulogic
restart    : std_uLogic
sensor     : std_uLogic
start      : std_uLogic
stop       : std_uLogic
testMode   : std_uLogic
coil1      : std_uLogic
coil2      : std_uLogic
coil3      : std_uLogic
coil4      : std_uLogic
testOut    : std_uLogic_vector(1 TO testNbLines)
    
```

Diagram Signals: