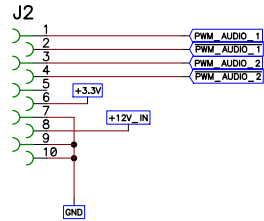
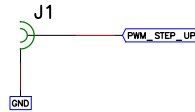


Connectors

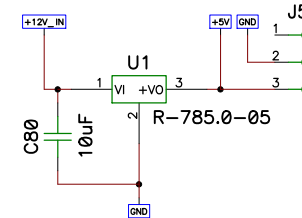
to Audio Amp



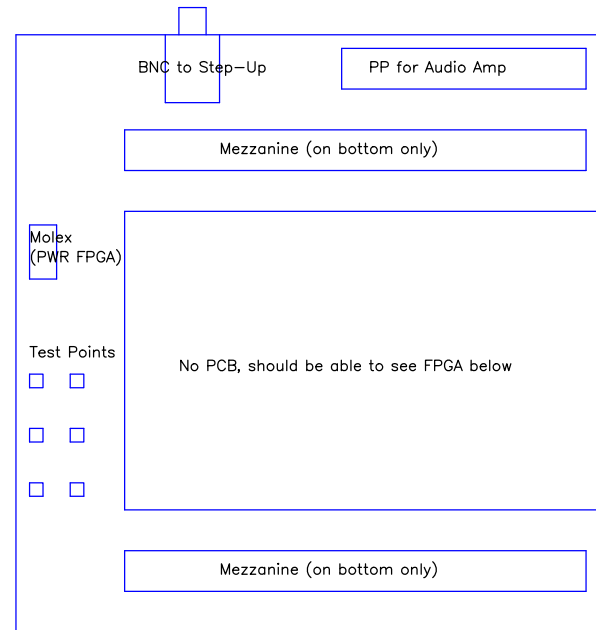
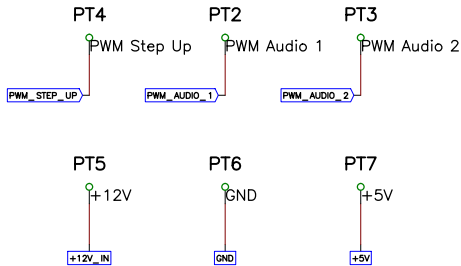
to Step-Up



Power for FPGA



Test Points

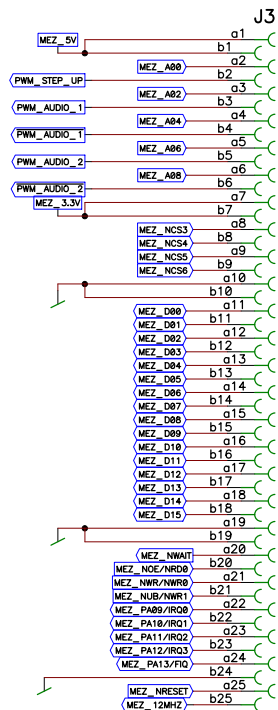


Audio Amp Mezzanine Adapter board	DES	20.12.2013 zas
Adapter	REV	v1.0
HAUTE ECOLE VALAISANNE	1/2	I:\Filiere\Promotion\EMVs\PCB Mezza_adapter_v1.0.sch

Mezzanine

Mezza A

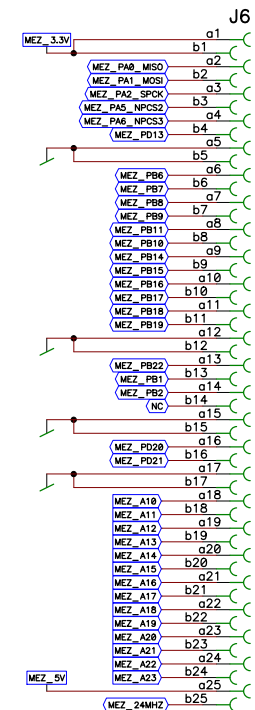
BOTTOM SIDE



39 point-to-point IOs
 + 1 global reset
 + 1 global clock (CLOCK)

Mezza B

BOTTOM SIDE



37 point-to-point IOs
 + 1 global clock (MCK0)

Audio Amp Mezzanine Adapter board	DES	20.12.2013 zas
Adapter	REV	v1.0
HAUTE ECOLE VALAISANNE	2/2	I:Filiere\Promotion\EMVs\PCB Mezza_adapter_v1.0.sch