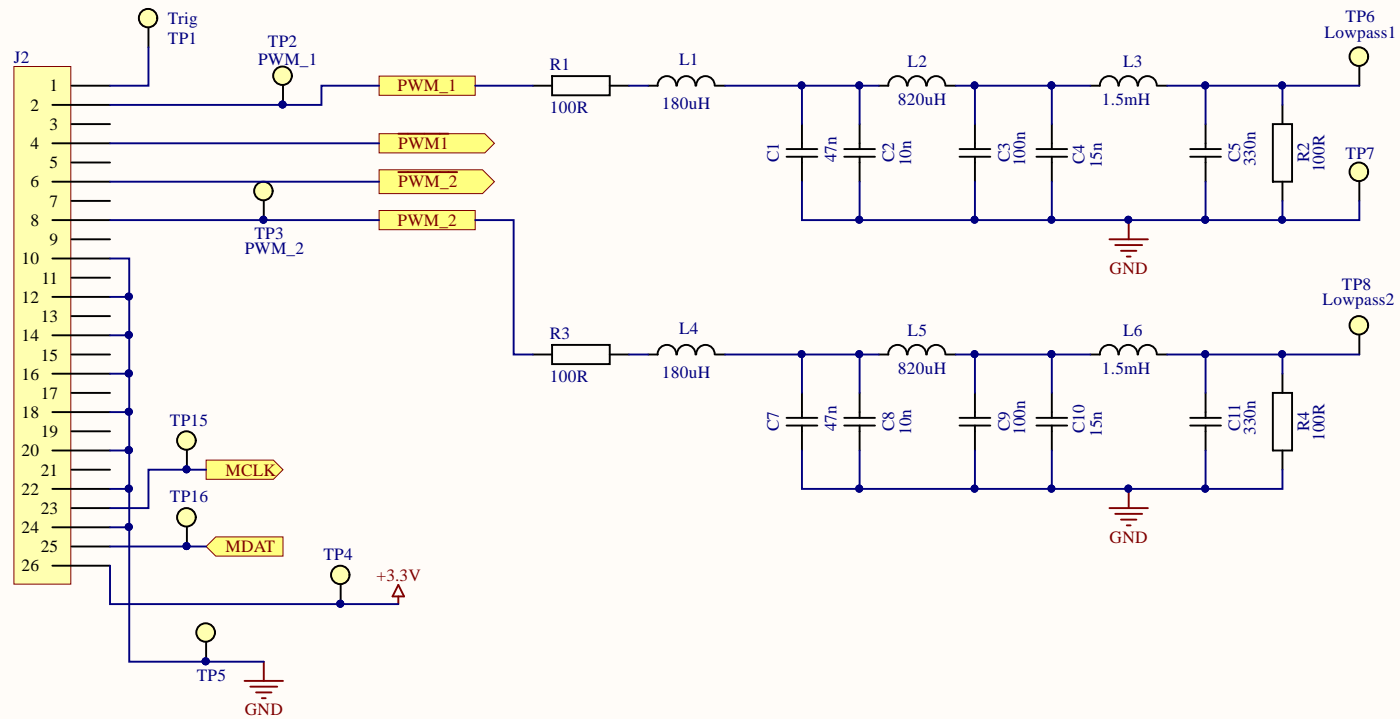


# PWM IN

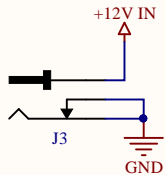
# Filtre passe-bas



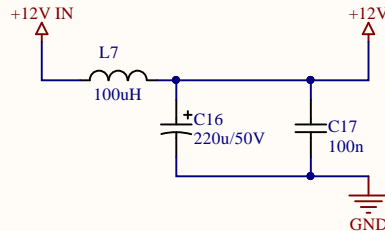
ProjetMaquetteOnduleur_EBS.PrjPcb			
Low filter.SchDoc		Date : 27.08.2021	
Revision : 1.0	Sheet 1 of 3	Design by : TAD	
I:\Admin\SI-ET\Institu\Infrastructure\Labos\A309\FPGA\Boards\Onduleur_EBS\Low filter.SchDoc			



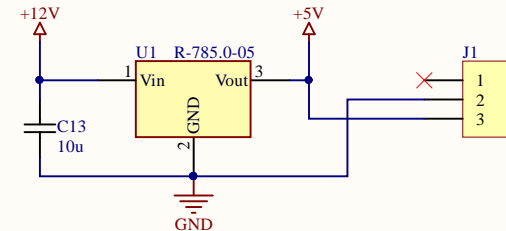
## Power Input



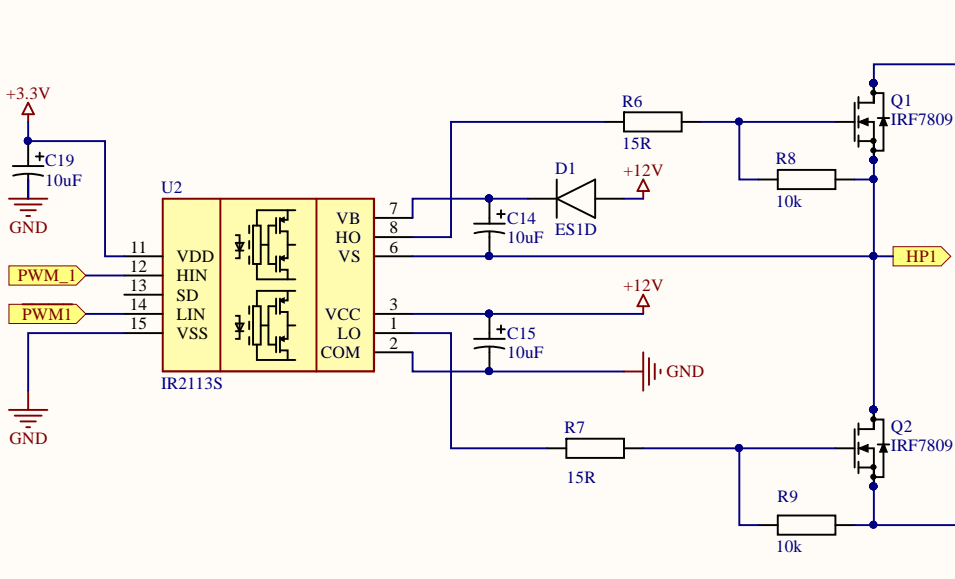
## Filtre 12V



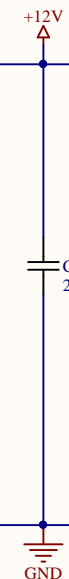
## Alimentation 5V pour carte FPGA



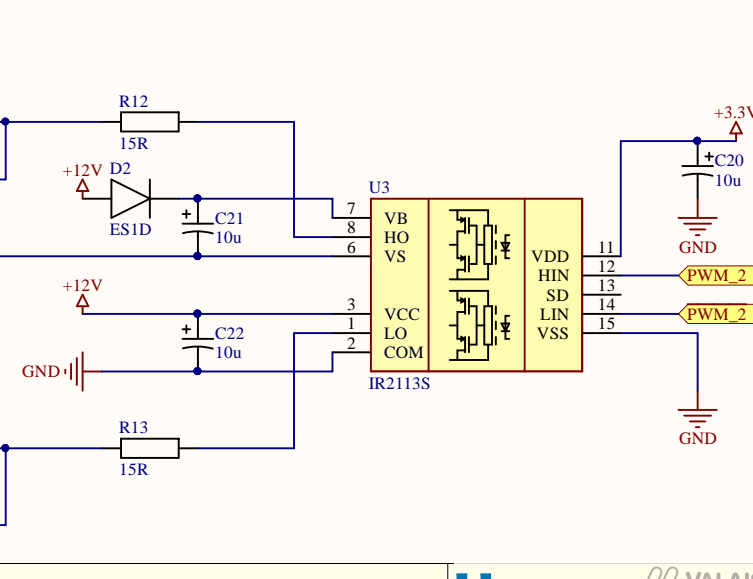
## Driver Pont en H



## Pont en H

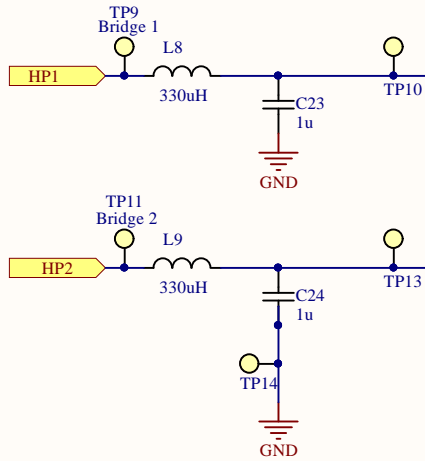


## Driver Pont en H

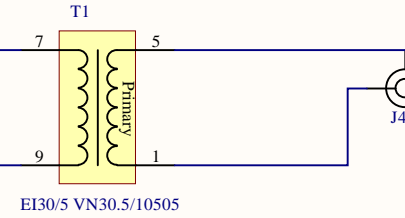


ProjetMaquetteOnduleur_EBS.PrjPcb Amplifier.SchDoc		Hes·so VALAIS WALLIS Date : 27.08.2021
Revision : 1.0	Sheet 2 of 3	Design by : TAD
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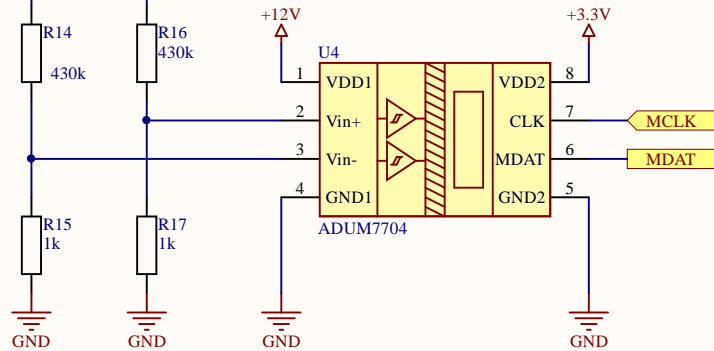
# Filtre LC out



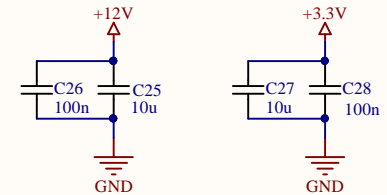
# 24V/230V



# Convertisseur A/D



# Découplage U4



ProjetMaquetteOnduleur_EBS.PrjPcb		Hes·SO VALAIS WALLIS	
Filtre LC +Output.SchDoc		Date : 27.08.2021	
Revision : 1.0	Sheet 3 of 3	Design by : TAD	
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